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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-32287

Kevin K. Main, et al.

Art Unit: 2111

Serial No.: 09/990,854

Examiner: Christopher A. Daley


Filed: November 9, 2001

Conf. No.: 2733

For: Low Pin Count (LPC) I/O Bridge

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Date May 6, 2005

FACSIMILE COVER SHEET

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u> </u> (<u> </u>) month (<u> </u> Page)
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NAME OF INVENTOR(S): Kevin K. Main, et al.	
TITLE OF INVENTION: Low Pin Count (LPC) I/O Bridge	
TI FILE NO.: TI-32287	DEPOSIT ACCT. NO.: 20-0668
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RECEIPT DATE & SERIAL NO.: Serial No.: 09/990,854 Filing Date: November 9, 2001	

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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2111

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Applicant: Main et al

Art Unit: 2111

Serial No.: 09/990,854

Examiner: Christopher A. Daley

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Docket: TI-32287

For: Low Pin Count (LPC) I/O Bridge

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Office on May 6, 2005.


Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 9, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: